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**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

Claim 1 (currently amended): A laminated ceramic electronic component comprising:

a laminated member including a plurality of stacked ceramic layers having at least two first ceramic layers and at least one second ceramic layer which is thinner than said at least two first ceramic layers; and

wiring conductors provided for a certain layer of said ceramic layers and including a via-hole conductor extending through said certain layer and a conductor extending along the principal surface of said certain layer; wherein

via-hole conductors of different ceramic layers have different sectional sizes; and the aspect ratio expressed by H/D is within the range of approximately 0.1 to approximately 3.0, wherein H and D represent the height and radial length for each of said via-hole conductors, respectively;

a first via-hole conductor extends through each of said at least two first ceramic layers;

a second via-hole conductor extends through said at least one second ceramic layer; and

the at least one second ceramic layer is disposed inside the laminated member, such that the at least one second ceramic layer is disposed between the at least two first ceramic layers and another layer of the plurality of ceramic layers.

Claim 2 (previously presented): A laminated ceramic electronic component according to Claim 1, wherein the sectional size of said first via-hole conductor is larger

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than that of said second via-hole conductor.

Claim 3 (canceled).

Claim 4 (currently amended): A laminated ceramic electronic component according to Claim 32, wherein a plurality of said via-hole conductors extending through the same ceramic layer have substantially the same sectional size.

Claim 5 (canceled).

Claim 6 (original): A laminated ceramic electronic component according to Claim 1, wherein said plurality of ceramic layers have substantially the same dielectric constant.

Claims 7-12 (canceled).

Claim 13 (currently amended): An electronic device comprising:  
a laminated ceramic electronic component; and  
a wiring board for mounting said laminated ceramic electronic component thereon;

wherein said laminated ceramic electronic component includes a laminated member having a plurality of stacked ceramic layers including at least two first ceramic layers and at least one second ceramic layer that is thinner than said at least two first ceramic layers;

wherein wiring conductors are provided for a specific ceramic layer and include a via-hole conductor extending through said specific ceramic layer and a conductor film extending along the principal surface of said specific ceramic layer;

via-hole conductors of different ceramic layers have different sectional sizes; and

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the aspect ratio expressed by H/D is within the range of approximately 0.1 to approximately 3.0, wherein H and D represent the height and radial length for each of said via-hole conductors, respectively;

a first via-hole conductor extends through each of said at least two first ceramic layers;

a second via-hole conductor extends through said at least one second ceramic layer; and

the at least one second ceramic layer is disposed inside the laminated member, such that the at least one second ceramic layer is disposed between the at least two first ceramic layers and another layer of the plurality of ceramic layers.

Claim 14 (previously presented): An electronic device according to Claim 13, wherein the sectional size of said first via-hole conductor is larger than that of said second via-hole conductor.

Claim 15 (canceled).

Claim 16 (currently amended): An electronic device according to Claim 15, wherein a plurality of said via-hole conductors extending through the same ceramic layer have substantially the same sectional size.

Claim 17 (canceled).

Claim 18 (original): An electronic device according to Claim 14, wherein said plurality of ceramic layers have substantially the same dielectric constant.

Claim 19 (previously presented): An electronic device according to claim 1, wherein a capacitor is provided on the laminated member.

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**Claim 20 (previously presented): An electronic device according to claim 13, wherein a capacitor is provided on the laminated member.**